

~~PATENT NUMBER~~

U.S. ~~UTILITY~~ Patent Application

O.I.P.E. A.C SCANNED 24 Q.A. <i>cc</i>	PATENT DATE
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APPLICATION NO. 09/585261	CONT/PRIOR F	CLASS <del>250</del> 361	SUBCLASS <del>7700</del>	ART UNIT <del>2878</del>	EXAMINER T. DINH
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## APPLICANTS

## Kash, John

### Circuit board assembly

**TITLE**

PTO-2040  
12/99[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.  <input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	_____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
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